

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1

of 1

Complete if Known

Application Number 09/927,457

Filing Date August 13, 2001

First Named Inventor L. FOURNIER

Group Art Unit 2121

Examiner Name

Attorney Docket Number FOURNIER=1

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Technology Center 2100

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
ck	AA	C. Kaner, "Software negligence and testing coverage, In proceedings of STAR 96: the Fifth International Conference, Software Testing, Analysis and Preview, pages 299-327, (1996)	
ck	AB	B. BEIZER, "software Testing Techniques. Van Nostrand Reinhold Cohn, 1990	
ck	AC	GRINWALD et al., "User Defined Coverage - A Tool Supported Methodology for Design Verification", <u>Proceedings of the 35th Design Automation Conference (DAC)</u> , pages 158-163, (1998)	
ck	AD	FOURNIER et al., "Functional Verification Methodology of Microprocessors Using the Genesys Test-Program Generator", <u>Functional Verification Methodology</u> , pages 1-7, (1998)	
ck	AE	LEWIN et al., "Constraint Satisfaction for Testing Program Generation", <u>Int. Phoenix Conference on Computers and Communications</u> , (1995). 4 pages	
ck	AF	AHARON et al., "Test Program Generation for Functional Verification of PowerPC Processors in IBM", <u>32 Design Automation Conference, San Francisco</u> , pages 279-285, (1995)	
ck	AG	LICHTENSTEIN et al., "Model Based Test Generation for Processor Verification", <u>Innovative Applications for Artificial Intelligence (IAAI), AAAI</u> , (1994), 12 Pages	
ck	AH	AAGAARD et al., "Formal Verification of Iterative Algorithms in Microprocessors", <u>Interl Corporation, Hillboro, Oregon, USA, DAC</u> , (2000), pages 200-206	
ck	AI	O'LEARY et al., "Formally Verifying IEEE Compliance of Floating-Point Hardware", <u>Intel Technology Journal</u> , vol. 1999-Q1, pages 1-14, (1999)	
ck	AJ	CLARKE et al., "Verifying the SRT Division Algorithm Using Theorem Proving Techniques", <u>Formal Methods in System Design</u> , vol. 14, pages 7-44, (1999)	
ck	AK	RUSSINOFF, "A mechanically Checked Proof of Correctness of the AMD K5 Floating Point Square Root Microcode", <u>Formal Methods in System Design</u> , vol. 14, pages 75-125, (1999)	
ck	AL	B. MARICK, "The Craft of Software Testing Subsystem Testing Including Object-based and Object oriented Testing. Prentice-Hall, (1995)	

Examiner
Signature

Charles Knudsen

Date
Considered

8/5/2004

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.